



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re New Patent Application of)
Tokuhiko TAMAKI et al.)
Japanese Priority Application No. 2000-302277) Attn: Applications
Japanese Priority Date: October 2, 2000) Branch
For: SEMICONDUCTOR INTEGRATED)
CIRCUIT DEVICE AND METHOD FOR)
FABRICATING THE SAME) Date: September 28, 2001

INFORMATION DISCLOSURE STATEMENT


Honorable Commissioner for Patents
Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the references listed on the attached Form PTO-1449 be made of record in the above-identified application.

Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Respectfully submitted,


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